

Electronic Acknowledgement Receipt

EFS ID:	6037900
Application Number:	10719020
International Application Number:	
Confirmation Number:	9168
Title of Invention:	Method for preparing a circuit board material having a conductive base and a resistance layer
First Named Inventor/Applicant Name:	Akira Matsuda
Customer Number:	38834
Filer:	Shuji Yoshizaki/Maya Takakura
Filer Authorized By:	Shuji Yoshizaki
Attorney Docket Number:	032130
Receipt Date:	09-SEP-2009
Filing Date:	24-NOV-2003
Time Stamp:	15:00:25
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$810
RAM confirmation Number	1070
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	032130_RCETransmittal_09_09_2009.pdf	161450 b45b3f71902958ed6cceb7987a1b7014bbb65a3c	no	3
Warnings:					
This is not a USPTO supplied RCE SB30 form.					
Information:					
2		032130_Submission114_09_09_2009.pdf	336420 374ca94cfabb9528c2cf9d521b946bee7b277726	yes	11
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment Submitted/Entered with Filing of CPA/RCE		1	1	
	Claims		2	4	
	Applicant Arguments/Remarks Made in an Amendment		5	11	
Warnings:					
Information:					
3	Fee Worksheet (PTO-875)	fee-info.pdf	30107 a6484fee260b09123b3587f9af12d829e3c33e53	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			527977		
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